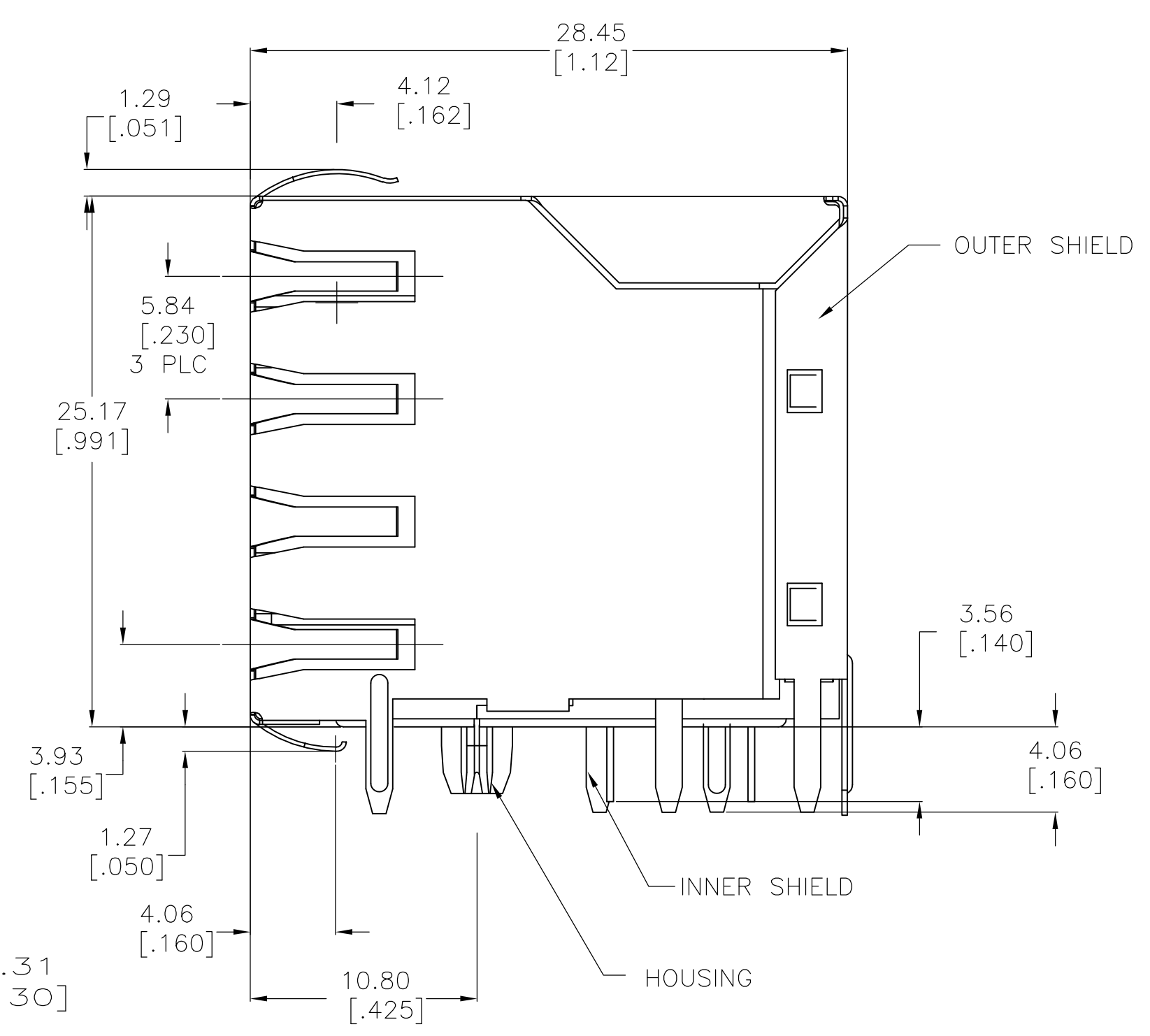
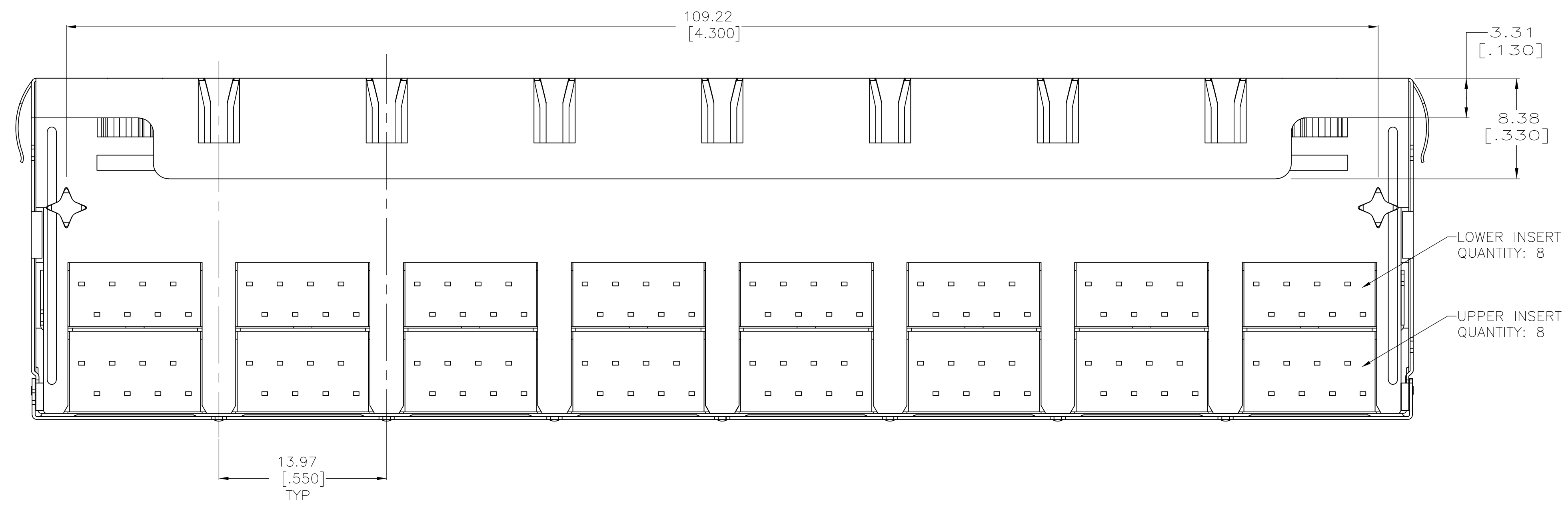
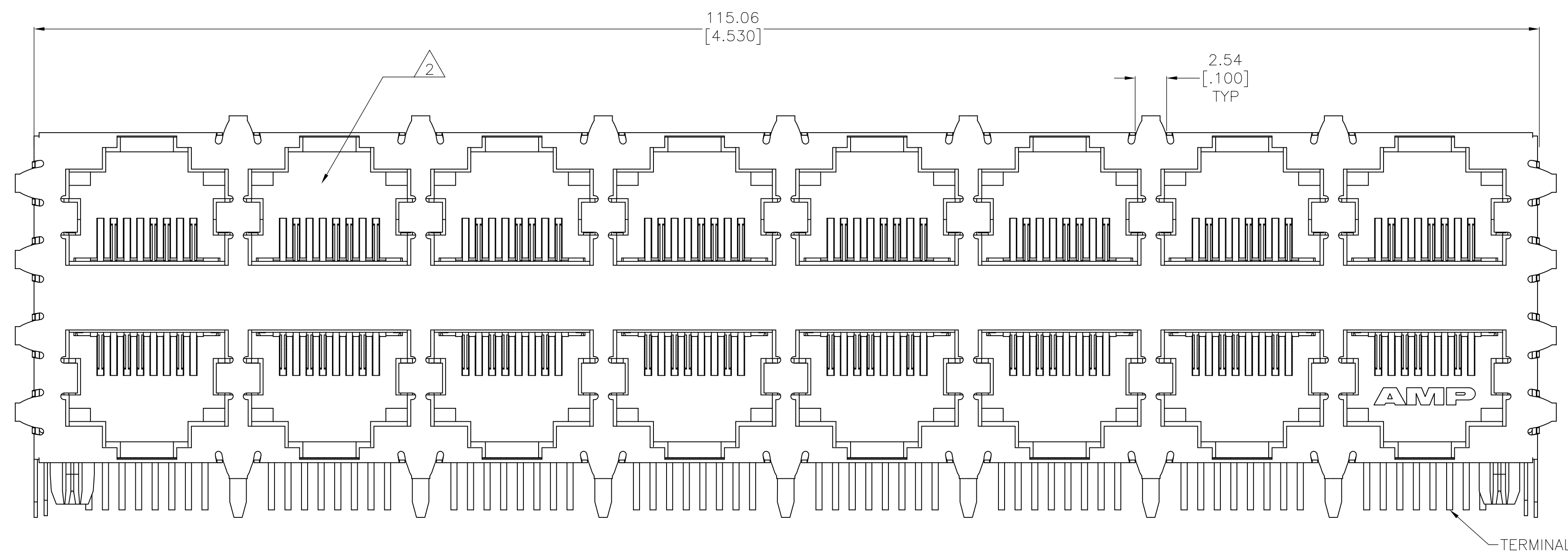
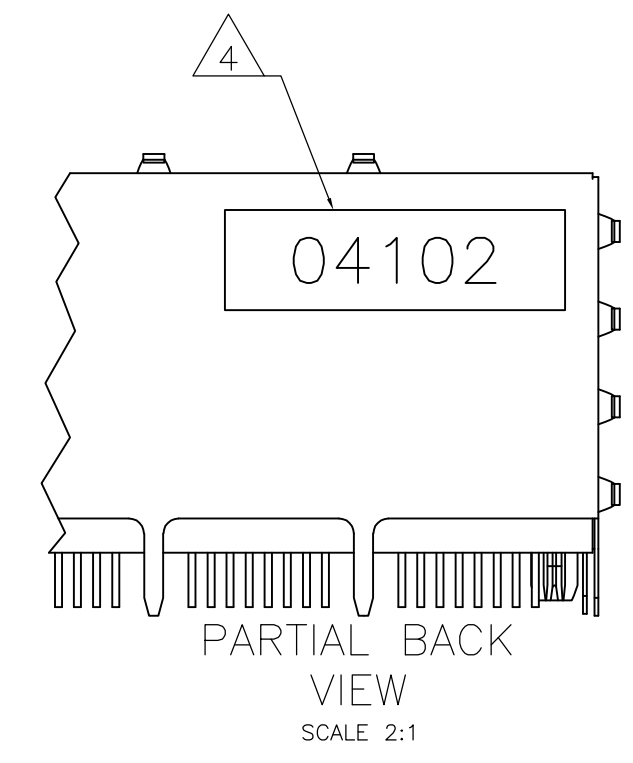


LOC		DIST		REVISIONS			
AA	00	REV	PER	DESCRIPTION	DATE	OWN	APVD
A1		REV PER	ECR-08-009376		5MAY2008	DZ	SY



- MATERIAL: HOUSING - PCT POLYESTER, BLACK, UL 94V-0. OVERMOLDED TERMINAL ARRAY - HIGH TEMPERATURE NYLON, COLOR: NATURAL. TERMINALS - 0.33[.013] THICK PHOS BRONZE PLATED WITH 1.27um[.000050] MIN THICK HARD GOLD IN LOCALIZED AREA AND 3.81um[.000150] MIN THICK MATTE TIN IN SOLDER AREA OVER 1.27um[.000050] MIN THICK NICKEL UNDERPLATE. SHIELD - 0.196[.0077] THICK COPPER ALLOY PLATED WITH SATIN NICKEL 1.27um[.000050] MIN THICK OVERALL AND 2.03um[.000080] MIN THICK HOT TIN DIP ON PCB GROUND TABS.
- JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F.
- USE 3.25mm OR #30 DRILL BIT WHEN PRODUCING THESE PCB HOLES.
- DATE CODE LOCATED ON REAR OF PART APPROXIMATELY AS SHOWN: FIRST 2 DIGITS ARE LAST 2 DIGITS OF YEAR, SECOND 2 DIGITS ARE MANUFACTURING WORK WEEK, LAST DIGIT IS DAY OF WEEK WITH SUNDAY = 1.
- PARTS PACKAGED IN SEALED BAGS WITH DESICCANT
- PARTS SUITABLE FOR LEAD FREE REFLOW PROCESSING TO 260° C
- THE PART NUMBER HAS NOT COMPLETED VALIDATION TESTING, THE STATUS IS PRELIMINARY.



THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN: L.V. L. MAYER 10JAN2008	Tyco Electronics Corporation Harrisburg, PA 17105-3608	
DIMENSIONS: mm [INCHES]		CHK: J. WESTMAN 10JAN2008	NAME: STACKED MODULAR JACK ASSEMBLY 2x8 PORT, 8 POSITION, SHIELDED, WAVE PANEL GROUND, CATEGORY 5	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD: S. FLICKINGER 10JAN2008	PRODUCT SPEC: 108-1163-2	
0 PLC ± -		APPLICATION SPEC: 114-2048		
1 PLC ± -		SIZE: A1		
2 PLC ± 0.25[.01]		CAGE CODE: 00779		
3 PLC ± 0.13[.005]		DRAWING NO: 2007372		
4 PLC ± -		CUSTOMER DRAWING		
ANGLES ± -		SCALE: 4:1		
FINISH: SEE NOTE 1		SHEET: 1 OF 2		
MATERIAL: SEE NOTE 1		REV: A1		

